

\$5 O.I.P.E. SCANNED <u>WU</u> G.A. <u>MC</u>	PATENT DATE
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LOCATION NO. 69/519719	CONT/PRIOR	CLASS 438 156	SUBCLASS 345	PART UNIT 212 1763	EXAMINER ALEJANDRO
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APPLICANTS

Temperature controlled semiconductor processing chamber liner

37

PTO-2040
12/89[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
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